

FIG. 1A

REPLACEMENT SHEET
Title: GENERATING ISOLATED BUS CYCLES FOR ISOLATED EXECUTION
 1st Named Inventor: Carl M. Ellison
 Application No.: 09/538,954 Docket No.: 42390P8107
 Sheet: 2/8

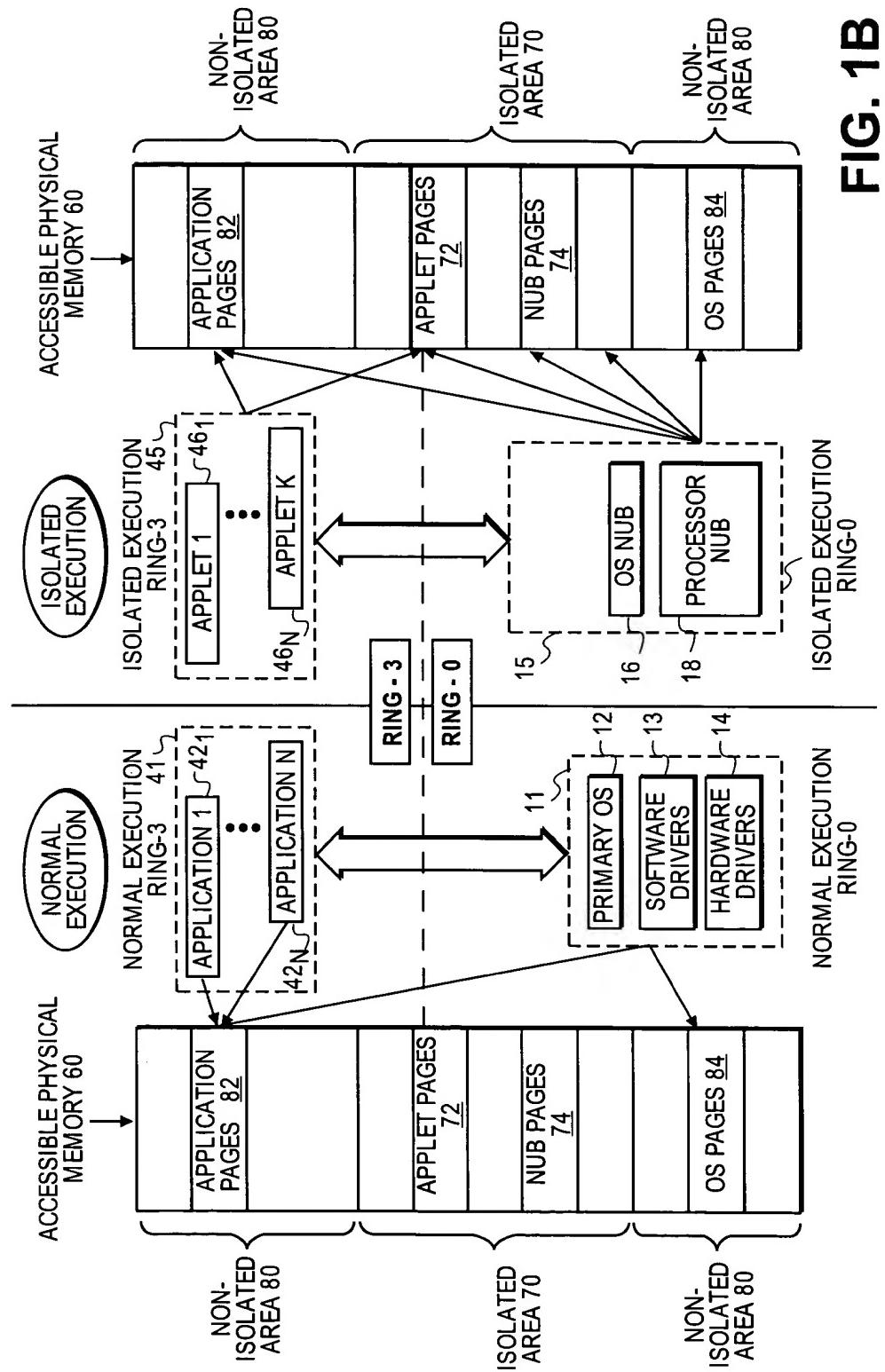


FIG. 1B

REPLACEMENT SHEET
 Title: GENERATING ISOLATED BUS CYCLES FOR ISOLATED
 EXECUTION
 1st Named Inventor: Carl M. Ellison
 Application No.: 09/538,954 Docket No.: 42390P8107
 Sheet: 3/8

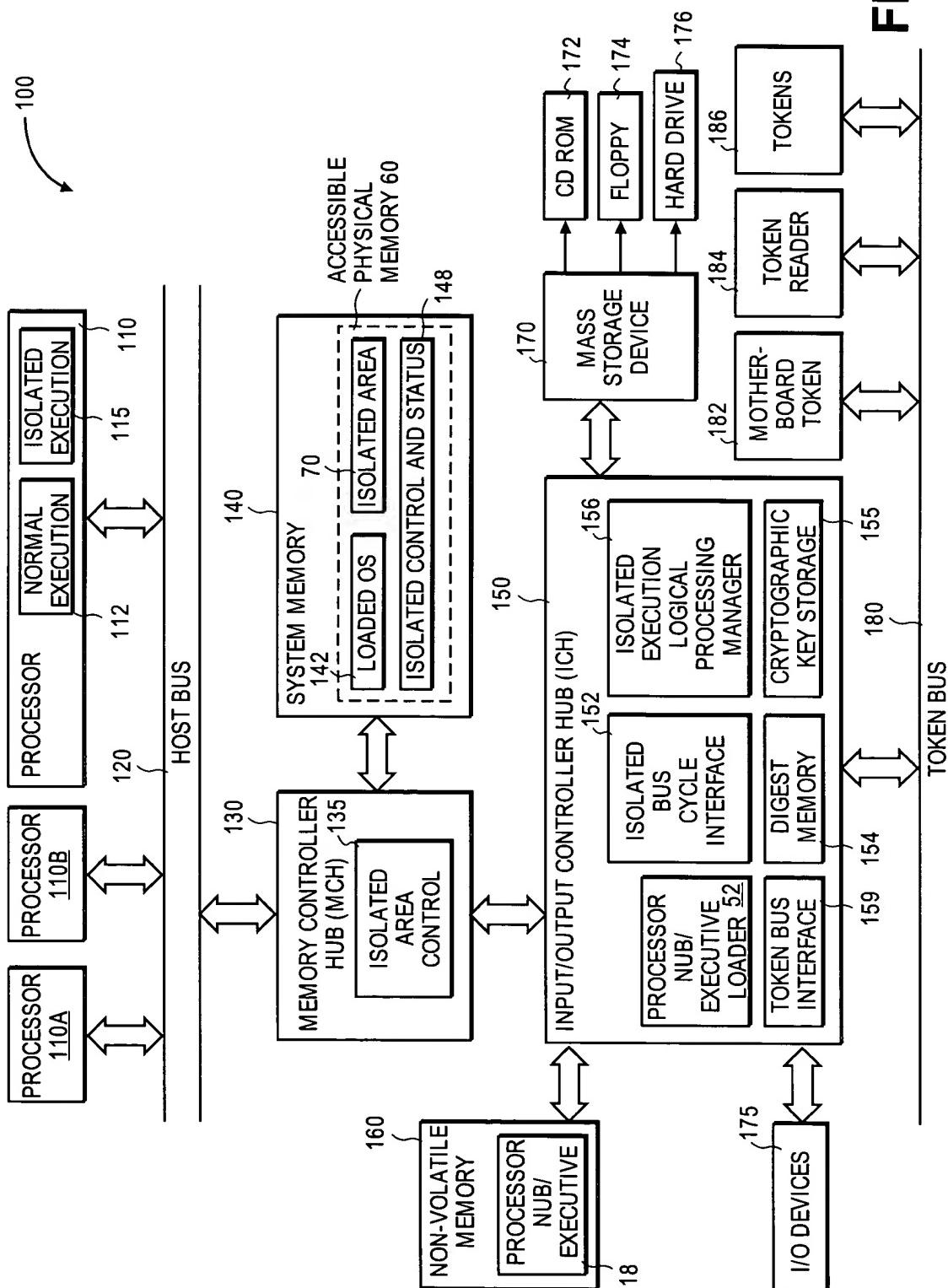


FIG. 1C

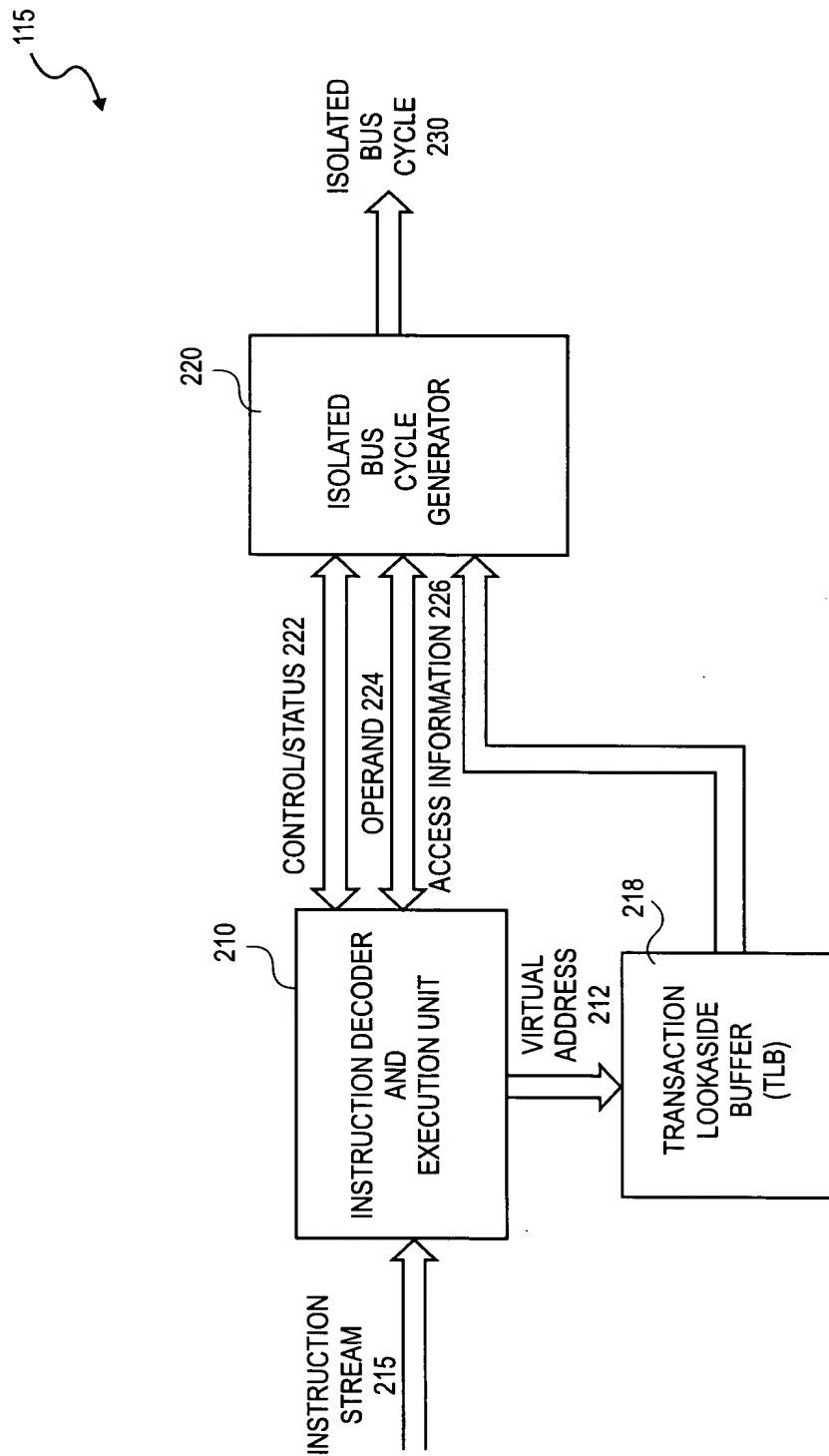


FIG. 2A

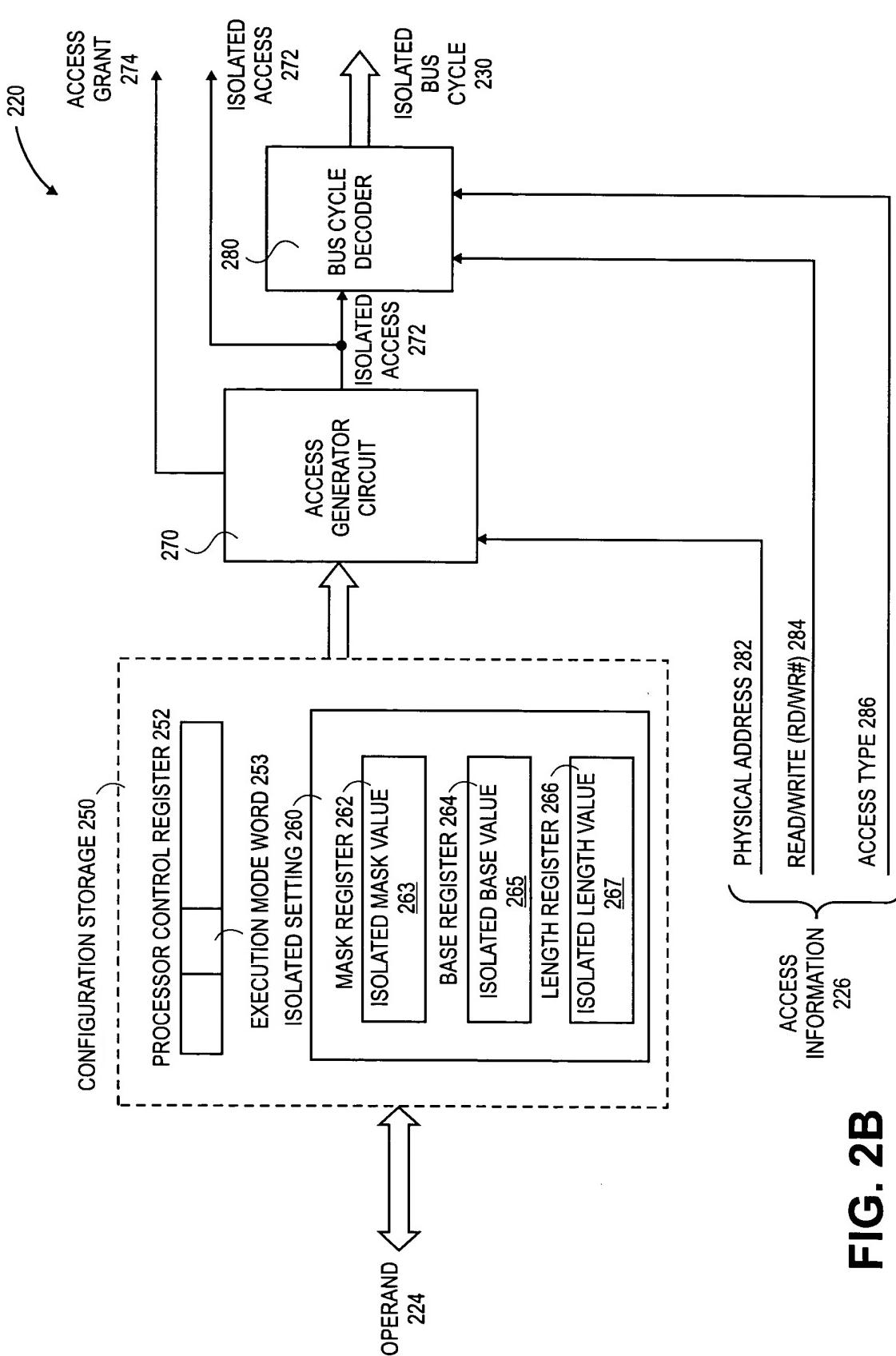


FIG. 2B

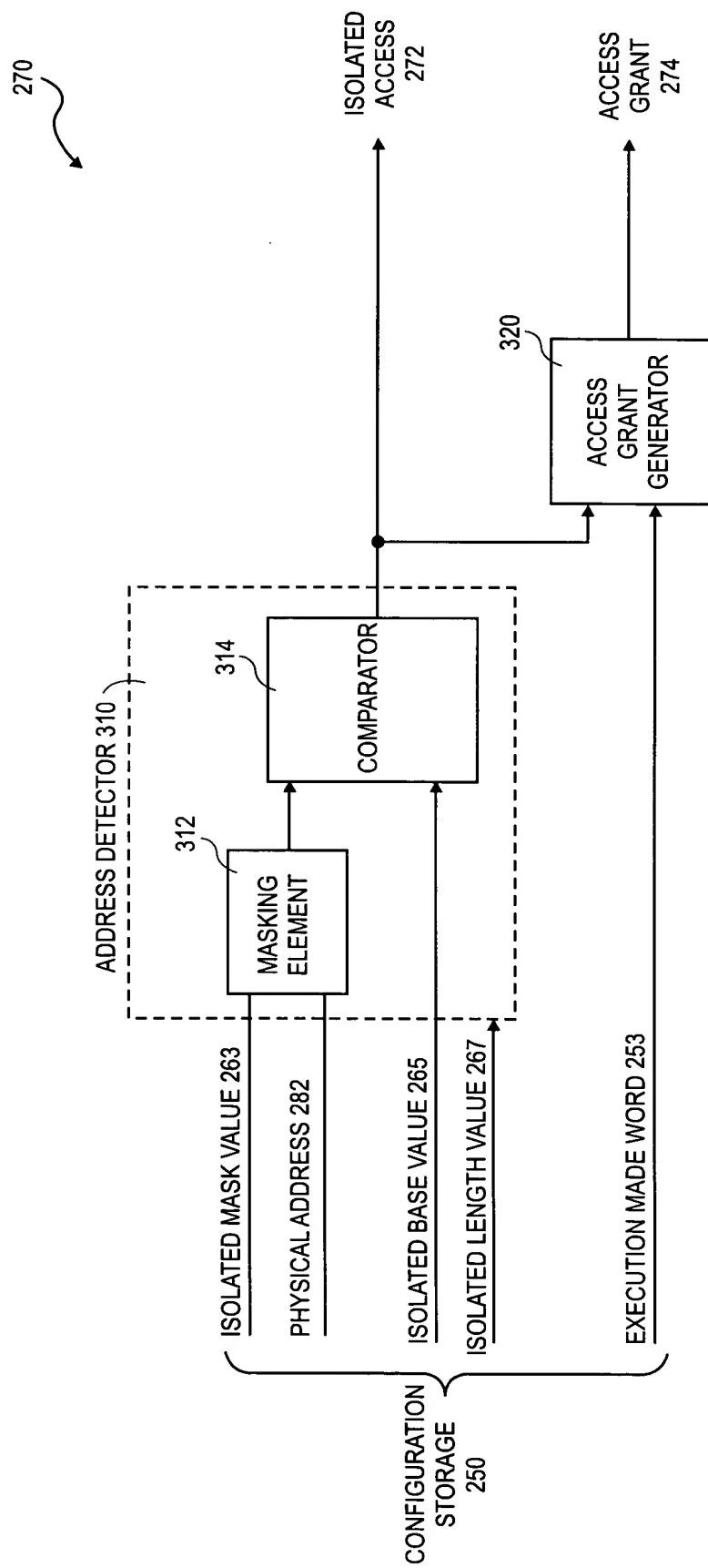


FIG. 3

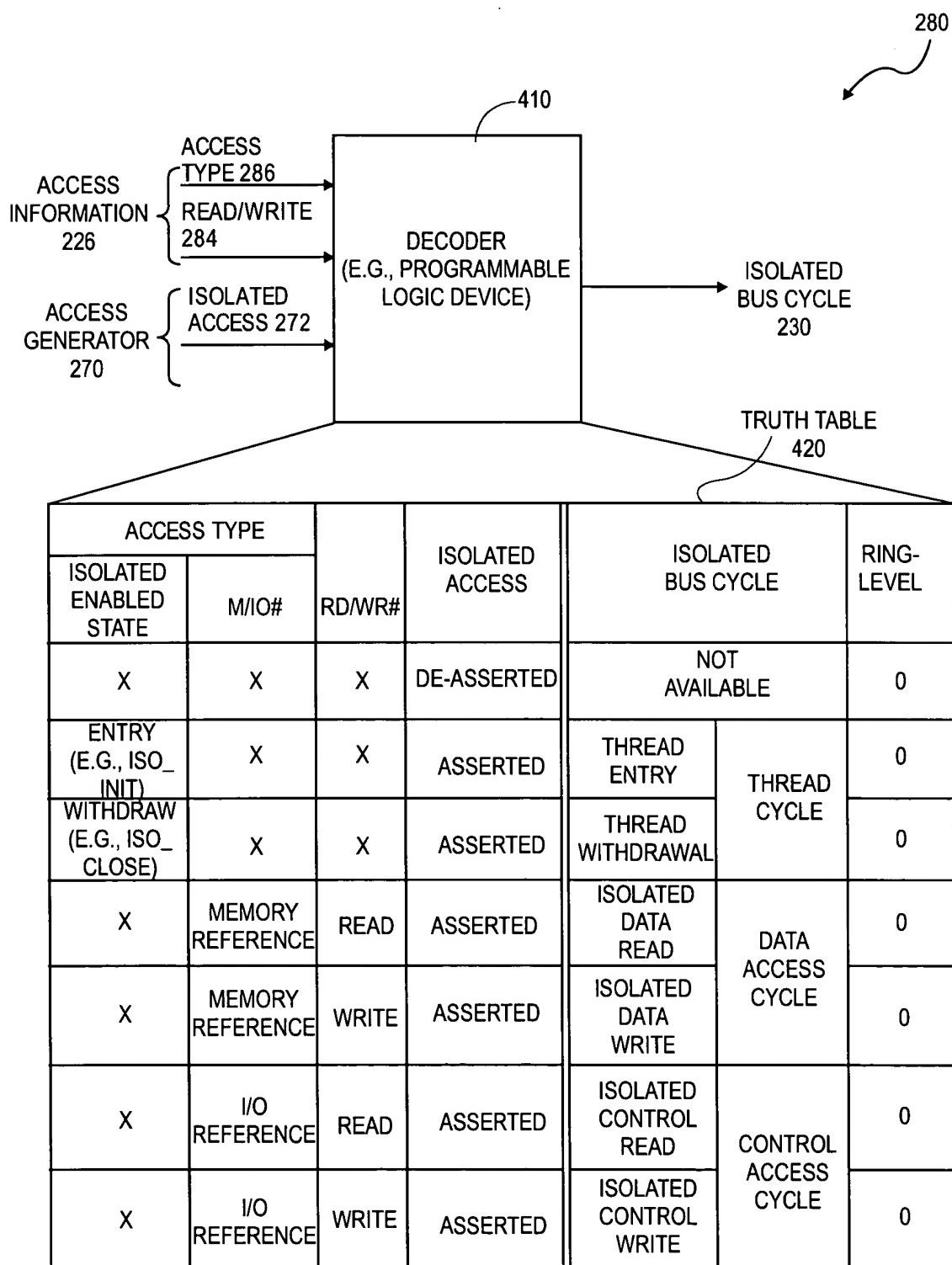


FIG. 4

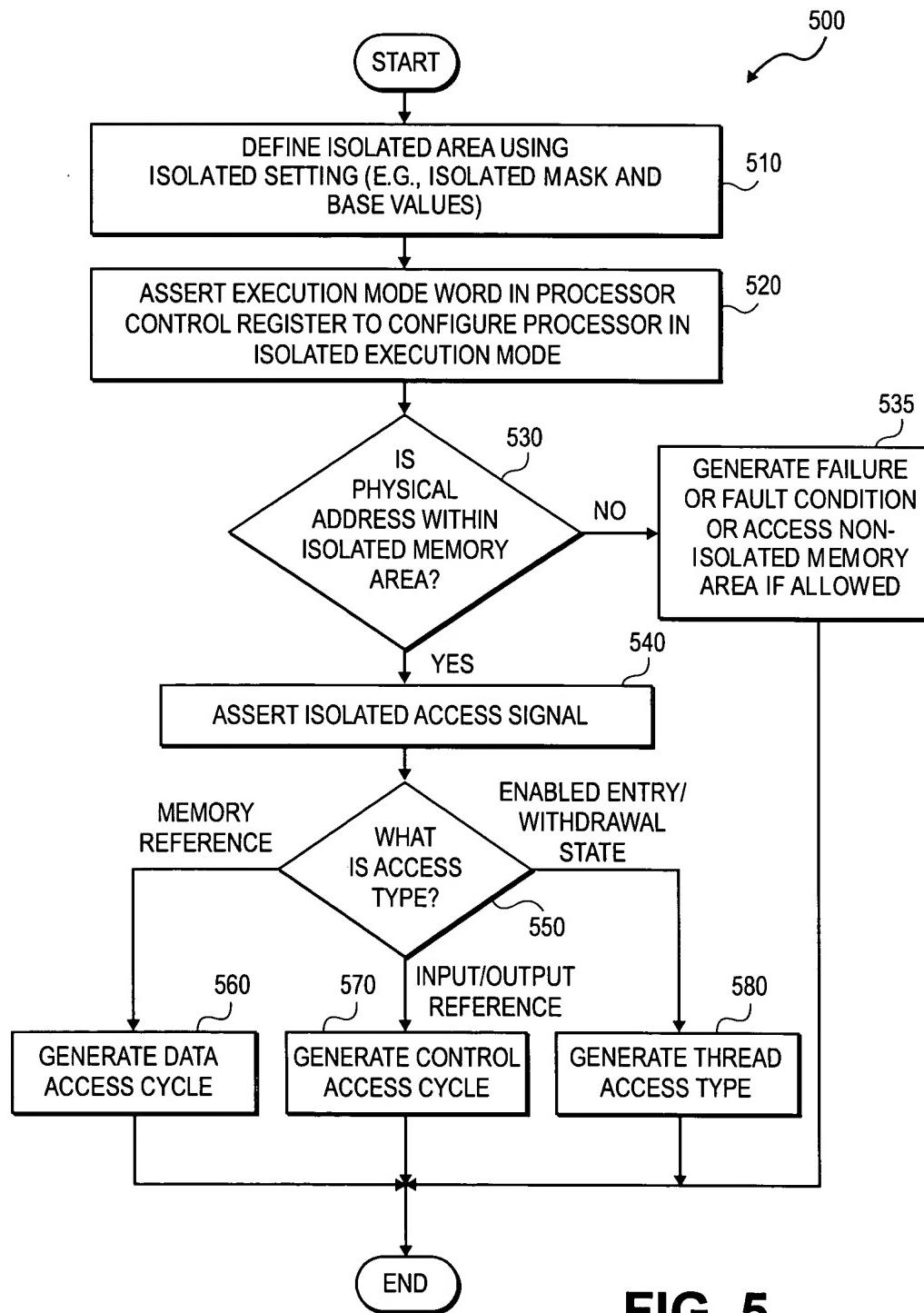


FIG. 5